@INPROCEEDINGS{Rigo2004SBAC,
author={Rigo, S. and Araujo, G. and Bartholomeu, M. and Azevedo,
R.},
booktitle={16th Symposium on Computer Architecture and High
Performance Computing, 2004 – SBAC-PAD 2004},
title={ArchC: a systemC-based architecture description language},
year={2004},
month=oct.,
volume={},
number={},
pages={ 66 – 73},
keywords={ formal verification; high level synthesis; instruction
set; open-source SystemC-based language; processor architecture
description language; computer architecture; electronic design
automation; formal verification; hardware description languages;
high level synthesis; instruction sets; logic testing;},
doi={10.1109/SBAC-PAD.2004.8},
ISSN={1550-6533 },
note={Best Paper Award},
}